Memory Module Specifications



KF584CU40RWA-24

24GB 3G x 64-Bit DDR5-8400 CL40 288-Pin CUDIMM



DEFAULT SPECIFICATIONS

CL	52 cycles
Row Cycle Time (tRCmin)	48ns(min.)
Refresh to Active/Refresh Command Time (tRFCmin)	410ns(min.)
Row Active Time (tRASmin)	32ns(min.)
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UL Rating	94 V - 0
UL Rating Operating Temperature	,

DESCRIPTION

Kingston FURY KF584CU40RWA-24 is a 3G x 64-bit (24GB) DDR5-8400 CL40 Clocked Unbuffered DIMMs (CUDIMMs) 1Rx8, memory module, based on eight 3G x 8-bit FBGA components per module. The module supports Intel® Extreme Memory Profiles (Intel® XMP) 3.0. Each module has been tested to run at DDR5-8400 at a low latency timing of 40-52-52 at 1.45V. The SPDs are programmed to JEDEC standard latency DDR5-6400 timing of 52-52-52 at 1.1V. Each 288-pin DIMM uses gold contact fingers. The JEDEC standard electrical and mechanical specifications are as follows:

DEFAULT FEATURES

- Power Supply: VDD = 1.1V Typical
- VDDQ = 1.1V Typical
- VPP = 1.8V Typical
- VDDSPD = 1.8V to 2.0V
- On-Die ECC
- Height 1.77" (45mm), w/heatsink

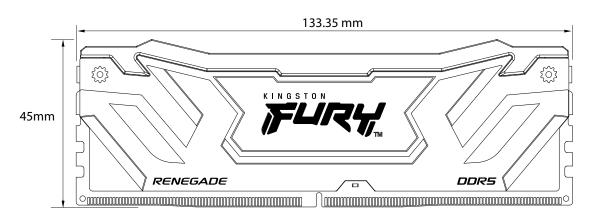
FACTORY TIMING PARAMETERS

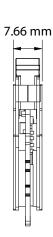
Default (JEDEC): DDR5-6400 CL52-52-52 @1.1V
 XMP Profile #1: DDR5-8400 CL40-52-52 @1.45V
 XMP Profile #2: DDR5-8000 CL38-48-48 @1.45V
 XMP Profile #3: DDR5-7600 CL38-46-46 @1.45V

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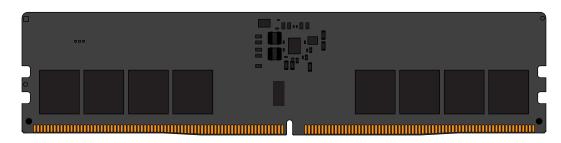


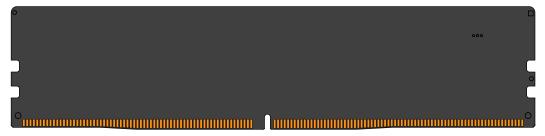
MODULE WITH HEAT SPREADER

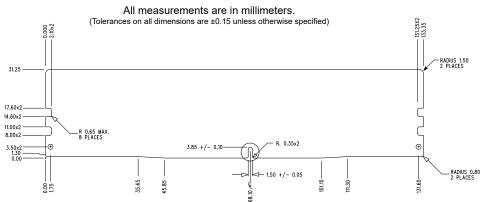




MODULE DIMENSIONS







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